

Appl. No. 10/519,765
Amdt. Dated July 23, 2007
Reply to Office action of June 1, 2006

AMENDMENTS TO THE CLAIMS:

This listing of claims will replace all prior versions, and listings, of claims in the application:

1. (Currently Amended) A module board device comprising:

a first organic substrate in which a conductive pattern or patterns is or are formed on the principal surface thereof, and one element body or more is or are mounted; and

a second organic substrate in which a recessed portion is formed in correspondence with the area where the element body or bodies is or are formed at a connecting surface to the first organic substrate,

whereby, in the state where the second organic substrate is connected to the first organic substrate, an element body accommodating space portion which seals the element body or bodies is constituted by the recessed portion, and the element body accommodating space portion is constituted in such a manner that moisture resistance characteristic and oxidation resistance characteristic are maintained, and

polishing processing is implemented to an insulating resin layer provided on at least one second principal surface of the first organic substrate and the second organic substrate of the side opposite to a surface where the first organic substrate and the second organic substrate are connected to each other so that a planarized

Appl. No. 10/519,765
Amdt. Dated July 23, 2007
Reply to Office action of June 1, 2006

(flattened) build-up formation surface is formed, and at least one build-up wiring layer or more is or are formed on the build-up formation surface.

2 (Original) The module board device as set forth in claim 1,

wherein the first organic substrate and the second organic substrate are formed by organic material having moisture resistance characteristic, or mixed material including such organic material.

3. (Original) The module board device as set forth in claim 1,

wherein a shield layer having moisture resistance characteristic and oxidation resistance characteristic is formed at the element mounting area of the first organic substrate and the recessed portion of the second organic substrate which constitute the element body accommodating space portion.

4. (Original) The module board device as set forth in claim 3,

wherein the shield layer consists of silicon oxide layer, silicon nitride layer, silicon carbide layer, boron nitride layer, or Diamond Like Carbon layer of at least one layer or more which can be formed as film under the low temperature condition.

5. (Original) The module board device as set forth in claim 3,

wherein the shield layer consists of at least one metallic layer or more, and serves to render electromagnetic wave resistance characteristic along with the

Appl. No. 10/519,765
Amdt. Dated July 23, 2007
Reply to Office action of June 1, 2006

moisture resistance characteristic and the oxidation resistance characteristic to the element body accommodating space portion.

6. (Original) The module board device as set forth in claim 1,

wherein an air vent hole communicating with the element body accommodating space portion is formed at the first organic substrate or the second organic substrate, and the air vent hole is sealed after air is deflated from the element body accommodating space portion to fill inactive gas.

7. (Original) The module board device as set forth in claim 1,

wherein the element body is Mechanical Electrical Micro System element, Surface Acoustic Wave filter element, high frequency element, or integrated circuit element having movable portion.

Claims 8-9 (Canceled)

10. (Currently Amended) The module board device as set forth in ~~claim 8~~
claim 1,

wherein the build-up wiring layer is constituted as a high frequency circuit portion in which at least one kind of ~~passive~~ element or elements is or are formed as film by the thin film technology or the thick film technology.

11. (Currently Amended) A method of manufacturing a module board device, including:

Appl. No. 10/519,765
Amdt. Dated July 23, 2007
Reply to Office action of June 1, 2006

a step of mounting one element body or more onto the principal surface of a first organic substrate ~~where~~ in which a conductive pattern or patterns has or have been formed; and

a step of connecting a second organic substrate, in which a recessed portion is formed in correspondence with the area where the element body or bodies is or are mounted at a connecting surface ~~to the first organic substrate,~~ to the first organic substrate in such a manner to seal the element body or bodies into an element body accommodating space portion constituted by the recessed portion ~~wherein~~ so that the element body accommodating space portion is constituted as a space portion in which moisture resistance characteristic and oxidation resistance characteristic are maintained,

a step of forming an insulating resin layer onto the entire surface of at least one second principal surface of the first organic substrate and the second organic substrate of the side opposite to a surface where the first organic substrate and the second organic substrate are connected to each other.

a step of implementing polishing processing to the insulating resin layer to allow the second principal surface to be a planarized (flattened) build-up formation surface, and

a step of forming one build-up wiring layer or more including at least one kind of passive element or more formed by the thin film technology or the thick film technology on the build-up formation surface.

Appl. No. 10/519,765
Amdt. Dated July 23, 2007
Reply to Office action of June 1, 2006

12. (Original) The method of manufacturing module board device as set forth in claim 11,

wherein the connecting step of the first organic substrate and second organic substrate is performed within inactive gas atmosphere.

13. (Original) The method of manufacturing module board device as set forth in claim 11,

wherein an air vent hole communicating with the element body accommodating space portion is formed at the first organic substrate or the second organic substrate, and subsequently to the connecting step of the first organic substrate and the second organic substrate, a step of deflating air within the element body accommodating space portion through the air vent hole, a step of filling inactive gas into the element body accommodating portion through the air vent hole, and a step of closing the air vent hole are implemented.

14. (Original) The method of manufacturing module board device as set forth in claim 11,

wherein the connecting step of the first organic substrate and the second organic substrate consists of a step of sticking adhesive sheet to either one connecting surface of the first organic substrate and the second organic substrate, a step of combining the first organic substrate and the second organic substrate after undergone positioning, and a step of allowing the first organic substrate and the

Appl. No. 10/519,765
Amdt. Dated July 23, 2007
Reply to Office action of June 1, 2006

second organic substrate to be in pressure contact with each other.

15. (Original) The method of manufacturing module board device as set forth in claim 11,

wherein the connecting step of the first organic substrate and the second organic substrate consists of a step of combining the first organic substrate and the second organic substrate after undergone positioning, and a step of applying ultrasonic wave to a connecting surface of the first organic substrate and the second organic substrate to weld it.

16. (Original) The method of manufacturing module board device as set forth in claim 11, including:

a step of forming a shield layer by shield material consisting of silicon oxide, silicon nitride, silicon carbide, boron nitride or Diamond Like Carbon of at least one layer or more which can be formed as film under low temperature condition at an element body mounting area and a recessed portion which constitute at least the element accommodating space portion of the first organic substrate and the second organic substrate,

wherein the element body accommodating space portion is constituted by the shield layer as a space portion in which moisture resistance characteristic and oxidation resistance characteristic are maintained.

17. (Original) The method of manufacturing module board device as set

Appl. No. 10/519,765
Amdt. Dated July 23, 2007
Reply to Office action of June 1, 2006

forth in claim 11, including

a step of forming a shield layer consisting of metallic film of at least one layer or more at an element body mounting area and a recessed portion which constitute at least the element accommodating space portion of the first organic substrate and the second organic substrate,

wherein the element accommodating space portion is constituted by the shield layer as a space portion in which electromagnetic wave characteristic is maintained along with moisture resistance characteristic and oxidation resistance characteristic.

Claim 18. (Canceled)

19. (Currently Amended) A high frequency module composed of:

a base substrate portion comprising a first organic substrate in which a conductive pattern or patterns is or are formed on the principal surface thereof and an element body or bodies is or are mounted, and a second organic substrate in which a recessed portion is formed in correspondence with the area where the element body or bodies is or are mounted at a connecting surface to the first organic substrate, whereby, in the state where ~~the first organic substrate and the second organic substrate are connected~~ is connected to the first organic substrate, an element body accommodating space portion which seals the element body or bodies is constituted by the recessed portion, the element body accommodating space

Appl. No. 10/519,765
Amdt. Dated July 23, 2007
Reply to Office action of June 1, 2006

portion is constituted ~~as a space portion in which~~ such a manner that moisture resistance characteristic and oxidation resistance characteristic are maintained, and a polishing processing is implemented onto an insulating resin layer provided on at least one second principal surface opposite to a connecting surface of either one of the first organic substrate and the second organic substrate constitutes a build-up formation surface; and of the first organic substrate and the second organic substrate of the side opposite to a surface where the first organic substrate and the second organic substrate are connected to each other so that a planarized (flattened) build-up formation surface is formed; and a high frequency circuit portion provided on the build-up formation surface of the base substrate portion.

~~a high frequency circuit portion composed of one build-up wiring layer or more in which a conductive pattern or patterns is or are formed on a dielectric insulating layer at the build-up formation surface of the base substrate portion, and including at least one kind of passive element or more formed by the thin film technology or the thick film technology, the build-up wiring layer being via connected to the base substrate portion and/or the element body or bodies, and high frequency circuit components (parts) mounted on the build-up wiring layer of the uppermost layer;~~

~~wherein the high frequency circuit portion is laminated and formed on the build-up formation surface of the base substrate portion to which planarization (flattening) processing has been implemented.~~

Appl. No. 10/519,765
Amdt. Dated July 23, 2007
Reply to Office action of June 1, 2006

wherein the high frequency circuit portion is constituted as a high frequency circuit portion comprising one build-up wiring layer or more including a conductive pattern or patterns formed on a dielectric insulating layer formed on the build-up formation surface, and at least one kind of passive element or more formed by the thin film technology or the thick film technology, the build-up wiring layer being via-connected to the base substrate portion and/or the element body or bodies, and high frequency circuit components (parts) mounted on the build-up wiring layer of the uppermost layer.

20. (Original) The high frequency module as set forth in claim 19,

wherein the element body mounted on the base substrate portion is one Mechanical Electrical Micro System switch or more, and is operative so that switching operation is performed to thereby switch capacity characteristic of capacity pattern formed at the build-up wiring layer of the high frequency circuit portion.

21. (Original) The high frequency module as set forth in claim 19,

wherein the element body accommodating space portion formed at the base substrate portion is constituted as a space portion in which electromagnetic wave resistance characteristic is maintained along with moisture resistance characteristic and oxidation resistance characteristic as the result of the fact that a shield layer consisting of at least one metallic layer or more is formed at the element mounting area of the first organic substrate and the recessed portion of the

Appl. No. 10/519,765
Amdt. Dated July 23, 2007
Reply to Office action of June 1, 2006

second organic substrate.

22. (Currently Amended) ~~A method of manufacturing a high frequency module, including;~~

~~a step of manufacturing a base substrate portion adapted so that an element body accommodating space portion is constituted as a space portion in which moisture resistance characteristic and oxidation resistance characteristic are maintained via a step of mounting an element body or bodies on the principal surface of a first organic substrate in which a conductive pattern or patterns is or are formed, a step of connecting a second organic substrate, in which a recessed portion is formed in correspondence with the area where the element body or bodies is or are mounted at a connecting surface to the first organic substrate, to the first organic substrate in such a manner to seal the element body or bodies within the element body accommodating space portion constituted by the recessed portion, and a step of implementing planarization (flattening) processing to a second principal surface opposite to the connecting surface of at least either one of the first organic substrate and the second organic substrate to form a build up formation surface; and~~

~~a step of forming a high frequency circuit portion via a step of forming one build up wiring layer or more in which a conductive pattern or patterns is or are formed on a dielectric insulating layer at the build up formation surface of the base substrate portion, and at least one kind of passive element or elements is or are~~

Appl. No. 10/519,765
Amdt. Dated July 23, 2007
Reply to Office action of June 1, 2006

~~formed by the thin film technology or the thick film technology, the build-up wiring layer being via connected to the conductive pattern or patterns and/or the element body or bodies of the first organic substrate, and a step of mounting high frequency circuit components (parts) on the build-up wiring layer of the uppermost layer.~~

A method of manufacturing a high frequency module, including:

a step of manufacturing a base substrate portion via a step of mounting an element body or more on the principal surface of a first organic substrate in which a conductive pattern or patterns is or are formed, a step of connecting a second organic substrate, in which a recessed portion is formed in correspondence with the area where the element body or bodies is or are mounted at a connecting surface to the first organic substrate, to the first organic substrate in such a manner to seal the element body or bodies within the element body accommodating space portion so that the element body accommodating space portion is constituted as a space portion in which moisture resistance characteristic and oxidation resistance characteristic are maintained, a step of forming an insulating resin layer onto the entire surface of at least one second principal surface of the first organic substrate and the second organic substrate of the side opposite to a surface where the first organic substrate and the second organic substrate are connected to each other, and a step of implementing polishing processing to the insulating resin layer to allow the second principal surface to be a planarized (flattened) build-up formation surface; and

Appl. No. 10/519,765
Amdt. Dated July 23, 2007
Reply to Office action of June 1, 2006

a step of forming a high frequency circuit portion via a step of forming one build-up wiring layer or more in which a conductive pattern or patterns is or are formed on a dielectric insulating layer formed at the build-up formation surface of the base substrate portion, and at least one kind of passive element or elements is or are formed by the thin film technology or the thick film technology, the build-up wiring layer being via-connected to the conductive pattern or patterns and/or the element body or bodies of the first organic substrate, and a step of mounting high frequency circuit components (parts) onto the build-up wiring layer of the uppermost layer.

23. (Original) The method of manufacturing high frequency module as set forth in claim 22,

wherein, at the step of manufacturing the base substrate portion, the connecting step of the first organic substrate and the second organic substrate is performed within inactive gas atmosphere.

24. (Original) The method of manufacturing high frequency module as set forth in claim 22,

wherein there is used the first organic substrate or the second organic substrate in which an air vent hole communicating with the element body accommodating space portion is formed, and, at the step of manufacturing the base substrate portion, as steps subsequent to the connecting step of the first organic substrate and the second organic substrate, a step of deflating air within the

Appl. No. 10/519,765
Amdt. Dated July 23, 2007
Reply to Office action of June 1, 2006

element body accommodating space portion through the air vent hole, a step of filling inactive gas into the element body accommodating space portion through the air vent hole, and a step of closing the air vent hole are implemented to hermetically introduce or fill inactive gas within the element body accommodating space portion.

25. (Original) The method of manufacturing high frequency module as set forth in claim 22,

wherein the connecting step of the first organic substrate and the second organic substrate consists of a step of sticking adhesive sheet to either one connecting surface of the first organic substrate and the second organic substrate, a step of combining the first organic substrate and the second organic substrate after undergone positioning, and a step of allowing the first organic substrate and the second organic substrate to be in pressure contact with each other.

26. (Original) The method of manufacturing high frequency module as set forth in claim 22,

wherein the connecting step of the first organic substrate and the second organic substrate consists of a step of combining the first organic substrate and the second organic substrate after positioning, and a step of applying ultrasonic wave to a connecting surface of the first organic substrate and the second organic substrate to weld it.

Appl. No. 10/519,765
Amdt. Dated July 23, 2007
Reply to Office action of June 1, 2006

27. (Original) The method of manufacturing high frequency module as set forth in claim 22,

wherein the step of forming the build-up wiring layer is adapted so that a first wiring layer is formed via a step of forming photosensitive dielectric layer onto the entire surface of the build-up formation surface of the base substrate portion, a via formation step and a step of forming a conductive pattern or patterns onto the photosensitive dielectric layer, and wiring layers of upper layers are formed in succession via similar steps onto the first wiring layer, and

a step of forming solder resist layer and an electrode formation step are implemented to the wiring layer of the uppermost layer to mount high frequency circuit components (parts).